

02-19-02

Docket No. AP01-005

U.S. PATENT AND TRADEMARK OFFICE  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: PETER W. LEE, HSING-YA TSAO, TAM TRAN, FU-CHANG HSU

10/07/02 PRO  
10971 U.S.

02/15/02

For: BIT-BY-BIT VT-CORRECTION OPERATION FOR NONVOLATILE SEMICONDUCTOR ONE-TRANSISTOR  
CELL, NOR-TYPE FLASH EEPROM

Enclosed are:

15 sheets of drawing(s) - formal.

An assignment of the invention to **Aplus Flash Technology, Inc.**

An associate power of attorney  Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	SMALL ENTITY
FOR:	NO. FILED	NO. EXTRA	RATE
BASIC FEE			\$ 370.
TOTAL CLAIMS	25 -20=	5	x 9 =
INDEP CLAIMS	4 -3=	1	x 42 =
MULTIPLE DEPENDENT CLAIM PRESENTED		+ 260 =	
		SUB TOTAL	\$ 457.
		ASSIGNMENT	\$40.
		TOTAL	\$ 497.

Please charge my Deposit Account No. 19-0033 in the amount of **\$ 497**. A duplicate copy of this sheet is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR §1.16.

Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761

## Express Mail Certificate

Express Mail No.EK830729395US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231. Applicant and/or Attorney requests the date of deposit as the Filing Date.

2/15/02

SBA 2/15/02

Date of deposit

Signature / Date

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**NONPUBLICATION REQUEST  
UNDER  
35 U.S.C. 122(b)(2)(B)(i)**

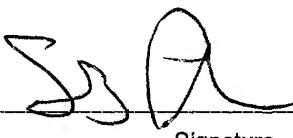
First Named Inventor		Peter W. Lee
Title	Bit-By-Bit VT-Correction Operation For Nonvolatile Semiconductor One Transistor Cell, Noritype Flash EEPROM	
Atty Docket Number	AF01-005	

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

2/15/02

Date



Signature

Stephen B. Ackerman, Reg#37,761  
Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**

Burden Hour Statement: This collection of information is required by 37 CFR 1.213(a). The information is used by the public to request that an application not be published under 35 U.S.C. 122(b) (and the PTO to process that request). Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 6 minutes to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.